

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of	)	
	)	
Koji NII et al.	)	Group Art Unit: Unassigned
	)	
Application No.: Unassigned	)	Examiner: Unassigned
	)	
Filed: December 6, 2001	)	
	)	
For: SEMICONDUCTOR DEVICE	)	

**PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination of the above-captioned patent application, kindly enter the following amendment.

**IN THE CLAIMS:**

*Kindly amend Claim 13, as follows.*

13. (Amended) A semiconductor device according to claim 1, comprising a SRAM cell, wherein the wiring layer is connected to the memory node of the SRAM cell.

**REMARKS**

No new matter has been introduced.

The wording in Claim 13 has been amended to avoid multiple dependency.

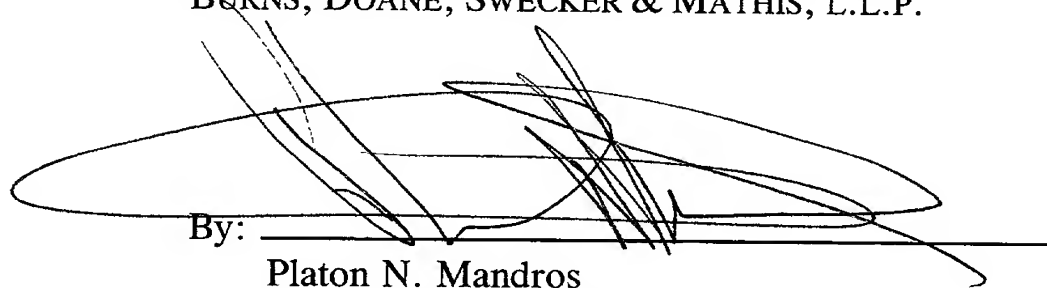
Early and favorable consideration with respect to this application is respectfully requested.

These changes have been made in accordance with 37 C.F.R. § 1.121 as amended on November 7, 2000. A marked-up version of Claim 13 indicating the changes is enclosed with this Preliminary Amendment.

Should any questions arise in connection with this application, the undersigned respectfully requests that he be contacted at the number indicated below.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By:   
Platon N. Mandros  
Registration No. 22,124

P.O. Box 1404  
Alexandria, Virginia 22313-1404  
(703) 836-6620

Date: December 6, 2001

**Attachment to Preliminary Amendment dated December 6, 2001**

## Marked-up Claims

13. (Amended) A semiconductor device according to claim 1 [or claim 4],  
comprising a SRAM cell, wherein the wiring layer is connected to the memory node of the  
SRAM cell.